ABSTRACT OF THE DISCLOSURE

A parallel processor that performs efficient parallel processing is provided. The parallel processor, which performs parallel

5 processing of one or more basic instructions contained in each of instruction words delimited by instruction delimiting information, includes: a plurality of instruction execution units that perform processes corresponding to supplied basic

10 instructions in parallel; an instruction fetch unit that fetches the instruction words one by one in accordance with the instruction delimiting information; and an instruction issue unit that issues each of the basic instructions contained in

15 each of the instruction words fetched by the instruction fetch unit to a corresponding one of the instruction execution units.